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By:

Date:

June 24, 2003

#9/Response
7/2/03
C.P.C.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Oliver Gehring et al.

Applic. No. : 10/045,278

Filed : January 14, 2002

Title : Method for Fabricating Embedded Nonvolatile Semiconductor Memory Cells

Examiner : Marcos D. Pizarro Crespo

Group Art Unit: 2814

R E S P O N S E

Hon. Commissioner for Patents,
Alexandria, VA 22313-1450

Sir :

Responsive to the Office Action dated April 22, 2003, the following remarks are submitted:

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